IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

REVOCATION OF POWER OF ATTORNEY, NEW POWER OF ATTORNEY AND NOTICE OF CHANGE OF CORRESPONDENCE ADDRESS

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

Marvell International Ltd., the owner of the entire right, title and interest in the attached listing of U.S. patents and pending applications hereby revokes all Powers of Attorney previously given in such patents and patent applications. Marvell International Ltd. is the Assignee of the entire right and interest in each of these U.S. patents and pending applications, as evidenced by the documentary evidence of the chain of title from the original owner to the Assignee, recorded in the Assignment records of the office at the reel/frame numbers indicated on the attached sheet.

Marvell International Ltd. hereby appoints the attorneys and/or agents associated with customer number 64768 to prosecute these patents and patent applications and transact all business in the U.S. Patent and Trademark Office.

Please send all correspondence and direct telephone calls to the following:

Gregory E. Stanton (Reg. No. 45,127) MARSHALL, GERSTEIN & BORUN LLP 233 S. Wacker Drive, Suite 6300 Sears Tower Chicago, Illinois 60606-6357 (312) 474-6300

The undersigned is authorized to act on behalf of Marvell International Lt.

For: Marvell International Ltd.

Name: CAROI

Dated: 6 DECEMBER 2006

Marvell International Ltd. U.S. Patents

Assignment	Reel/Frame No	Tool I will be a second of the	009844/0862		018515/0817		009672/0397			018515/0817		010419/0894			018515/0817					010603/0899			018515/0817	***************************************
ment	To	INTEL	CORPORATION	MARVELL	INTERNATIONAL	LTD.	INTEL	CORPORATION	MARVELL	INTERNATIONAL	LTD.	INTEL	CORPORATION		MARVELL	INTERNATIONAL	LTD.	INTEL	CORPORATION			MARVELL	INTERNATIONAL	LTD.
Assignment	From	CLARK, LAWRENCE	T.	INTEL	CORPORATION		O'CONNOR,	DENNIS M.	INTEL	CORPORATION		CLARK,	LAWRENCE T.;	TSANGARIS, E.J.M.	INTEL	CORPORATION		CLARK,	LAWRENCE T.;	CLARK,	MATTHEW M.	INTEL	CORPORATION	
Title		5-Volt Tolerant 3-Volt Drive Push-Pull	Buffer/Driver				Method and Apparatus for Speeding	Sequential Access of a Set-Associative				Register File with Improved Noise	Immunity and Aspect Ratio					Method and Apparatus to Perform a	Round Robin and Locking Cache	Replacement Scheme				
Attorney/	Docket No.	MP1529				A CDICOL	MF1526					MP1530						MP1532						
Patent	Date	3/13/2001				\$/22/2001	1007/77/6					8/28/2001					2/4/2003	7/4/2003				The second		
Patent No.		6,201,428				6 227 087	100,102,0				00.000	0,282,139					6 515 204	0,510,584						

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	Assignment	Pool/Frame Mo	010629/0127		018515/0817		012609/0901				018515/0817		014246/0808		018515/0817		
	ment	To	INTEL	MARVELL	INTERNATIONAL	LTD.	INTEL	CORPORATION		MARVELL	INTERNATIONAL	LTD.	INTEL	CORPORATION	MARVELL	INTERNATIONAL	LTD.
	Assignment	From	O'CONNOR,	INTEL	CORPORATION		FRANCO, AMIR;	YELLIN, DANIEL; GURESHNIK FRAN	, mary (1991)	INTEL	CORPORATION		TANG,	PING T.	INTEL	CORPORATION	
	Title		Method and Apparatus to Encode Digital Signals)		Time Dolon Extension	thing Delay Estimation in a Transmitter					Method and America F. 18	Acoustic of Fig. 1 B.	Committees of Fixed-Point	Computations		
	Attorney/	Docket No.	MP1531			MP1530	(C) 1 41.1					MP1486					
D. 1.	Farent	Date	4/1/2003			2/21/2006						4/18/2006)				
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Reel/Frame No	014533/0696	018515/0817	014224/0304	018515/0817	012793/0866	018515/0817
To	INTEL	MARVELL INTERNATIONAL LTD.	INTEL	MARVELL INTERNATIONAL LTD	INTEL CORPORATION	MARVELL INTERNATIONAL I TD
From	RAYKAR, VIKAS C.; KOZINTSEV, IGOR; LIENHART, RAINER	INTEL CORPORATION	KHAN, MOINUL H. PANG, SWEE- CHIN	INTEL CORPORATION	ERELL, ADORAM	INTEL CORPORATION
	Three-Dimensional Position Calibration of Audio Sensors and Actuators on a Distributed Computing Platform		Dynamic TLB Locking Based on Page Usage Metric		Enhancing the Intelligibility of Received Speech in a Noisy	Environment
Docket No.	MP1488		MP1487		MP1540.11	
Date	4/25/2006		7/25/2006		8/8/2006	
	7,035,757		7,082,508		7,089,181	

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Patent Attorney/ Title Assignme Date Docket No. From From 10/17/2006 MP1518 Crystal Oscillator with Variable Bias STEVENSON, PAUL Generator and Variable Loop Filter E.; TOURVILLE, JON E.; PIHLSTON, NATHAN L. CORPORATION CORPORATION		Assignment	Dool/Ename Me	NCCULIABILE INO.	016024/0112					018515/0817		
Patent Attorney/ Title Date Docket No. From 10/17/2006 MP1518 Crystal Oscillator with Variable Bias STEVENSON, P. Generator and Variable Loop Filter E.; TOURVILLE, JO PIHLSTON, NAT L. INTEL		ment	T	10	INTEL	CORPORATION				MARVELL	INTERNATIONAL	LTD.
Patent Attorney/ Date Docket No. 10/17/2006 MP1518		Assigni	From	TITOTY	STEVENSON, PAUL	ij	TOURVILLE, JON E.;	PIHLSTON, NATHAN	L.	INTEL	CORPORATION	
Patent Date 10/17/2006					Crystal Oscillator with Variable Bias	Generator and Variable Loop Filter						
Patent Date 10/17/2006	V 444	Automey/	Docket No.	Maniero	MILISIS							
7,123,109			Date	10/17/2006	0007//1/01							
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Marvell International Ltd. U.S. Patent Applications

Assignment	Reel/Frame No.	013468/0665		014047/0317		018515/0817		013627/0281		014047/0317		018515/0817		014002/0599		018515/0817	
ment	To	D.S.P.C. TECHNOLOGIES	LTD.	INTEL CORPORATION		MARVELL INTERNATIONAL	LID.	D.S.P.C. TECHNOLOGIES	LID.	INTEL	CORPORATION	MARVELL	INTERNATIONAL LTD.	INTEL	CORPORATION	MARVEL	INTERNATIONAL LTD
Assignment	From	MAZUZ, GADI; GREISAS, YUVAL;		D.S.P.C. TECHNOLOGIES	LTD.	INTEL CORPORATION		ARONOWITZ, HAGAI		D.S.P.C.	TECHNOLOGIES LTD.	INTEL	CORPORATION	DARROUDI,	KHOSRO; MEARS, BRIAN R.	INTEL	CORPORATION
Title		Cipher Implementation						Method and Apparatus for Adapting Reference Templates						Speech Compression Method and	Apparatus		
Attorney	Docket No.	MP1546						MP1473						MP1475			
Application	Filing Date	11/4/2002					000000	12/24/2002						1/3/2003			
lon		10/286,800					10/02/050	10/32/,038						10/336,668			

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Assimment	Reel/Frame No	013878/0351	018515/0817	014664/0419	018515/0817	014875/0896	018515/0817	015114/0345	018515/0817
ment	То	INTEL	MARVEL INTERNATIONAL I TD	INTEL	MARVELL INTERNATIONAL I TD	INTEL	MARVELL INTERNATIONAL LTD	INTEL	MARVELL INTERNATIONAL I TD
Assignment	From	O'CONNOR, DENNIS	INTEL CORPORATION	YELLIN, DANIEL	INTEL CORPORATION	SUTSKOVER, ILAN; BEN-ELI, DAVID	INTEL	MUNTER, JOEL D.; NANDA, MUTHI; GUO, ZHIYAO; XU, JIN J.	INTEL
Title		Register File with Integrated Routing to Execution Units for Multi-Threaded Processors		Unified MMSE Equalization and Multi- User Detection Approach for Use in a	CDMA System	Transmitter Operations for Interference Mitigation		Power and/or Energy Optimized Compile/Execution	
Attorney	Docket No.	MP1481		MP1483		MP1484		MP1502	
Application	Filing Date	3/11/2003		10/30/2003		12/29/2003		3/17/2004	
Application	No.	10/388,384		10/697,853		10/749,693		10/802,586	

															
Assignment	Keel/Frame No.	014869/0564	014869/0875	014860/0561	1000/000110		018515/0817		015168/0523					018515/0817	
	01	INTEL CORPORATION	CORPORATION	INTEL	CORPORATION		MARVELL INTERNATIONAL	LTD.	INTEL CORPORATION					MAKVELL	OT I
	TOTOTI CHENT	nsien, Cheng- HSUEH	BEYLIN, BORIS	DAI	JASON		INTEL CORPORATION		BIBIKAR, VASUDEV J.;	FULLERTON,	MARK N.;	reduelek, James R	MITTER	CORPORATION	*****
	Debugging Tool for Debugging Multi	Threaded Programs						Downer	rower Supply detection Method, Apparatus, and System						
Docket No.	MP1499					**************************************		MP1506	00CI 1141						
Filing Date	4/29/2004		Maria de la companya					6/30/2004					•		
	10/835,083							10/880,762							
	Filing Date Docket No.	Filing Date Docket No. From From To for Debugging Multi-	Filing Date Docket No. 4/29/2004 MP1499 Debugging Tool for Debugging Multi- HSIEH, CHENG- INTEL Threaded Programs HSUEH CORPORATION	4/29/2004 MP1499 Debugging Tool for Debugging Multi- HSIEH, CHENG- INTEL HSUEH CORPORATION BEYLIN, INTEL CORPORATION INTEL CORPORATION INTEL CORPORATION INTEL BORIS CORPORATION	4/29/2004 MP1499 Debugging Tool for Debugging Multi- HSIEH, CHENG- INTEL HSUEH Threaded Programs HSUEH CORPORATION BEYLIN, INTEL BORIS CORPORATION DAI, INTEL	Filing Date Docket No. 4/29/2004 MP1499 Debugging Tool for Debugging Multi- HSIEH, CHENG- INTEL Threaded Programs BEYLIN, INTEL BORIS CORPORATION DAI, INTEL JASON CORPORATION	4/29/2004 MP1499 Debugging Tool for Debugging Multi- HSIEH, CHENG- INTEL HSUEH CORPORATION BEYLIN, INTEL BORIS CORPORATION DAI, INTEL INTEL BORIS CORPORATION DAI, INTEL INTEL INTEL BORIS CORPORATION DAI, INTEL IN	Filing Date Docket No. From From To 4/29/2004 MP1499 Debugging Tool for Debugging Multi- HSIEH, CHENG- INTEL Threaded Programs BEYLIN, CORPORATION BORIS CORPORATION DAI, INTEL INTEL MARVELL CORPORATION INTERNATIONAL	Hing Date Docket No. 4/29/2004 MP1499 Debugging Tool for Debugging Multi- HSIEH, CHENG- INTEL HSUEH CORPORATION BEYLIN, INTEL BORIS CORPORATION DAI, INTEL MARVELL CORPORATION INTEL MARVELL CORPORATION INTEL MARVELL CORPORATION LTD.	4/29/2004 MP1499 Debugging Tool for Debugging Multi- From From From Tool Tool Invited 4/29/2004 MP1499 Debugging Tool for Debugging Multi- HSIEH, CHENG- INTEL From Tool for Debugging Multi- HSIEH, CHENG- INTEL BORIS CORPORATION DAI, INTEL JASON CORPORATION 6/30/2004 MP1506 Power Supply detection Method, BIBIKAR, INTEL Apparatus, and System VASUDEV J.; CORPORATION INTEL VASUDEV J.; CORPORATION	Home Date Docket No. 429/2004 MP1499 Debugging Tool for Debugging Multi-HSIEH, CHENG-CORPORATION BEYLIN, BORIS CORPORATION DAI, INTEL JASON 6/30/2004 MP1506 Power Supply detection Method, BIBIKAR, INTEL Apparatus, and System FULLERTON, CORPORATION FULLERTON, CORPORATION FULLERTON, CORPORATION FULLERTON, CORPORATION FULLERTON,	Hilling Date Docket No. 429/2004 MP1499 Debugging Tool for Debugging Multi-HSIEH, CHENG-TORPORATION Threaded Programs HSUEH CORPORATION BEYLIN, DAI, INTEL BORIS CORPORATION DAI, INTEL JASON CORPORATION LTD. CORPORATION LTD. CORPORATION LTD. HSIEMAR, INTEL Apparatus, and System From To	From From	From From To	From From To

Assignment	Reel/Frame No	015582/0651		018515/0817	018197/0599	018515/0817	
ment	To	INTEL		MARVELL INTERNATIONAL	LID. INTEL CORPORATION	MARVELL INTERNATIONAL	LID.
Assignment	From	ANDERS, MARK A. MATTHEW, SANU	K.; KRISHNAMURTHY, RAM K.	INTEL CORPORATION	ERELL, ADORAM	INTEL	
Title		Distributed Ring Control Circuits for Viterbi Traceback			Enhancing the Intelligibility of Received Speech in a Noisy Environment		
	_	MP1519			MP154011C1		
Application Application	Filing Date	9/30/2004			8/2/2006		
Application	10,051	10/954,648			11/498,458		